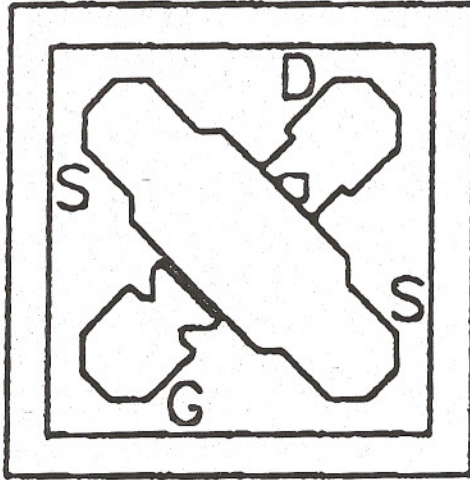




# Sierra Components, Inc.

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 Phone: 775.783.4940 Fax: 775.783.4947

Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below.



**Topside Metal: Aluminum**  
**Backside Metal: Gold**  
**Backside Potential: Isolated**  
**Mask Ref: Issue 1**  
**Bond Pads (Mils): .004" min**

**APPROVED BY:**  
**MFG: Celeritek Corp.**

**DIE SIZE: .0177" X .0177"**  
**THICKNESS: .008"**

**DATE: 5/5/00**  
**P/N: CF018-11**

DG 10.1.2  
 Rev A 3-4-99

Post-it® Fax Note	7671	Date	# of pages ▶
To	Mike Cole	From	Dennis
Co./Dept.		Co.	SCI
Phone #		Phone #	
Fax #	Note Backside	Fax #	Potential correction